

FIG. 1

PERFORM POSITION CONTROL TO  
SET GAP BETWEEN QUARTZ AND  
WAFER AT 0.1mm

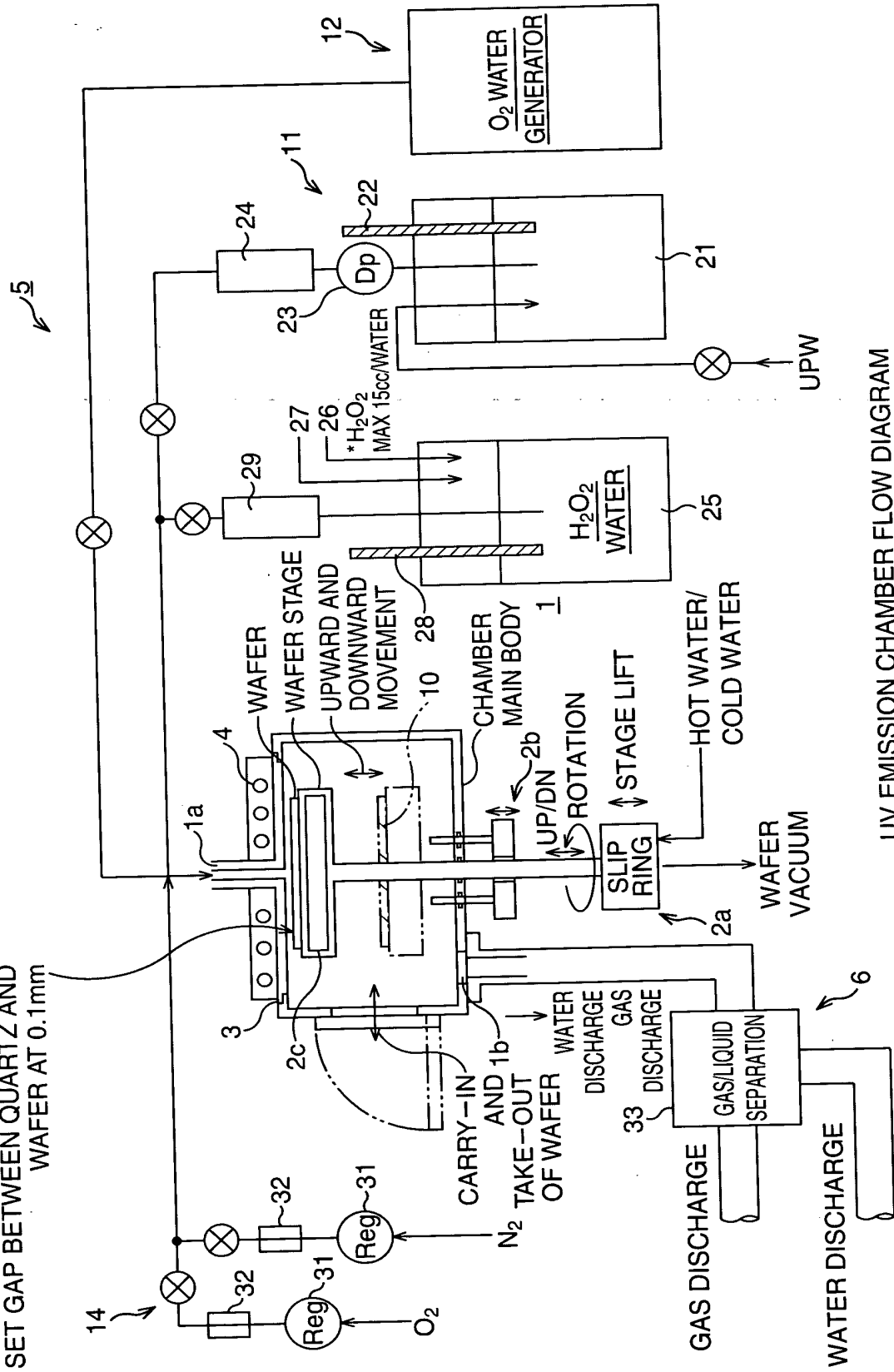


FIG. 2

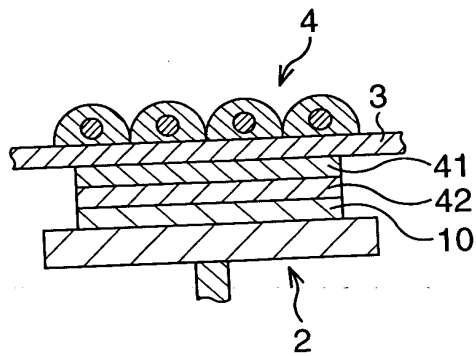


FIG. 3

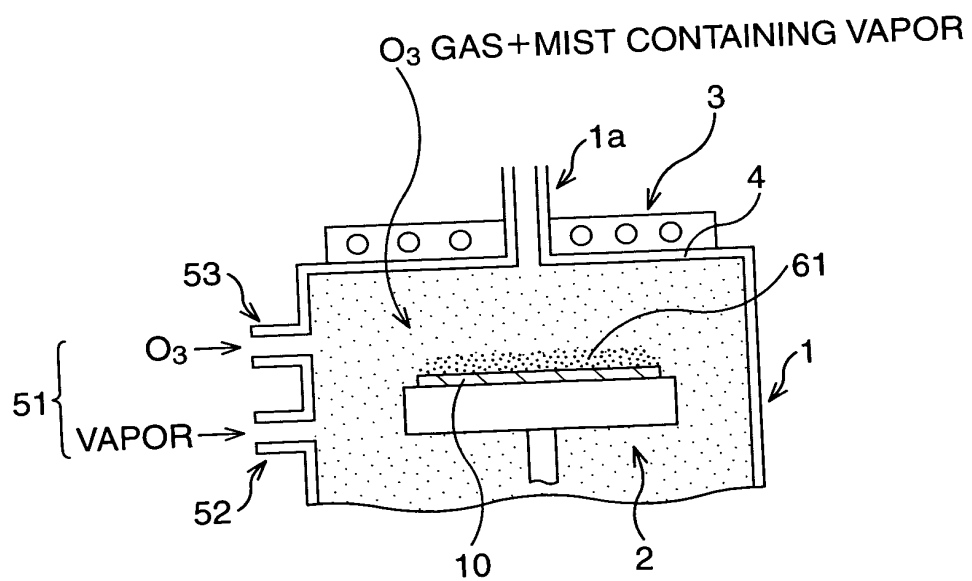


FIG. 4

